



1/10

FIG. 1

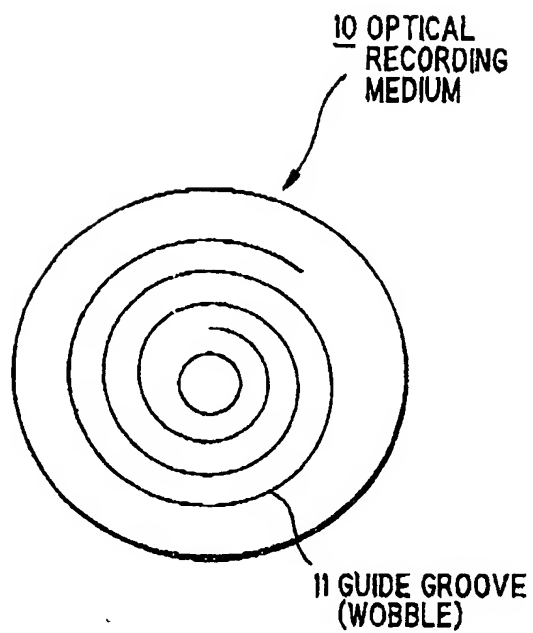


FIG. 2(A)

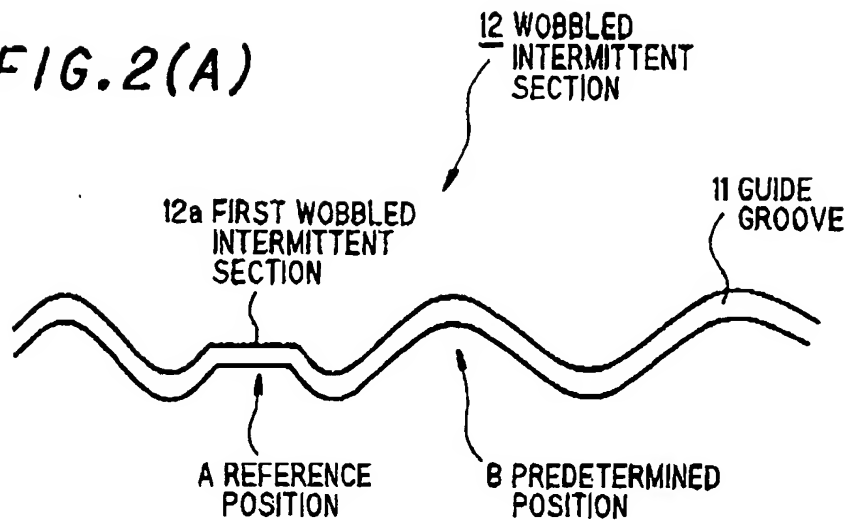


FIG. 2(B)

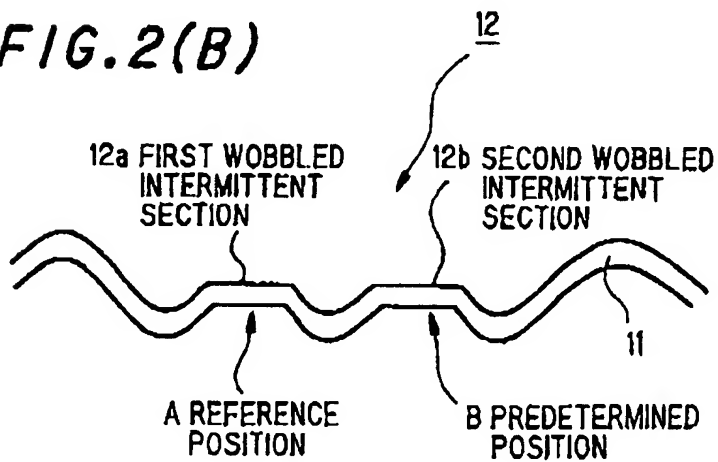


FIG. 3

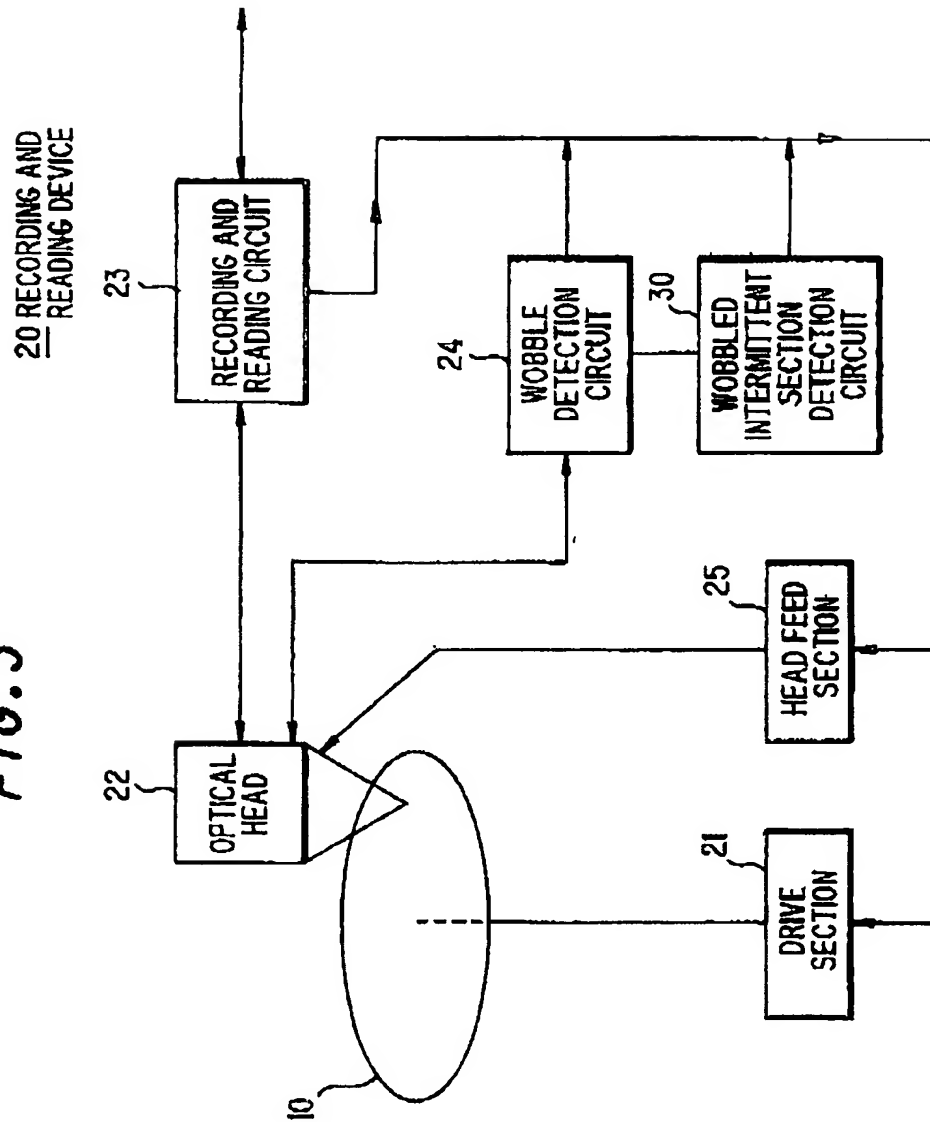


FIG. 4
30 WOBBLED INTERMITTENT
DETECTION CIRCUIT

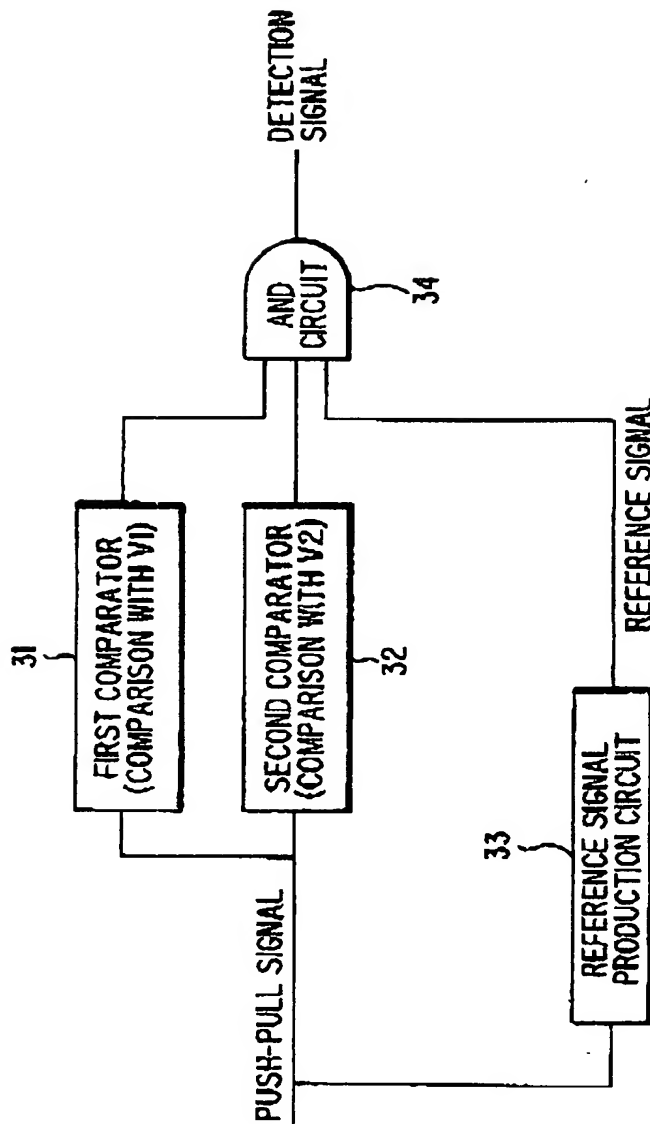
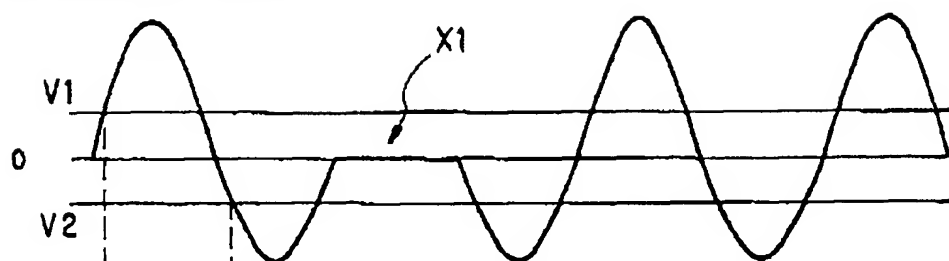
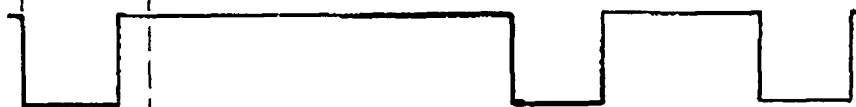


FIG. 5

(A) PUSH-PULL SIGNAL



(B) OUTPUT OF COMPARATOR 31



(C) OUTPUT OF COMPARATOR 32



(D) REFERENCE SIGNAL



(E) DETECTION SIGNAL



FIG. 6

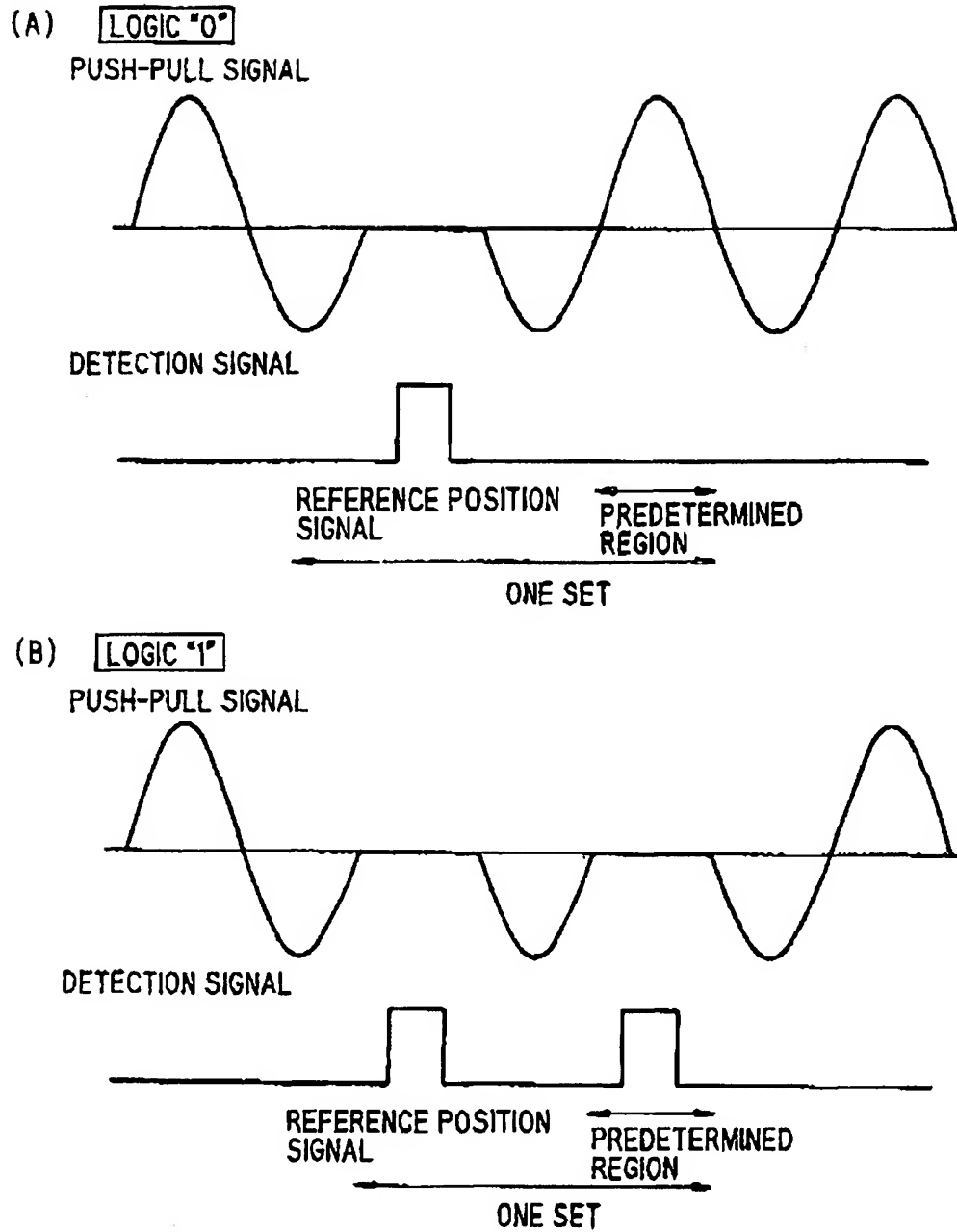


FIG. 7(A)

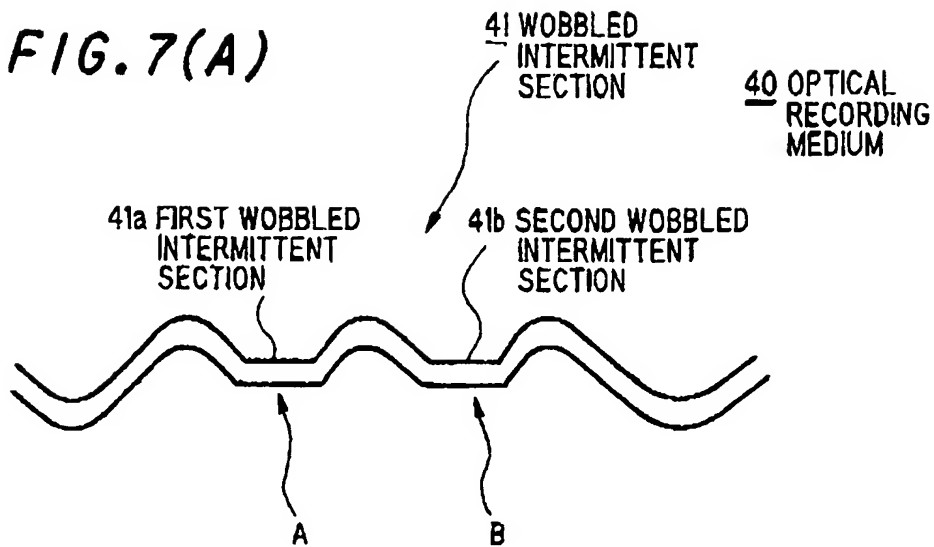


FIG. 7(B)

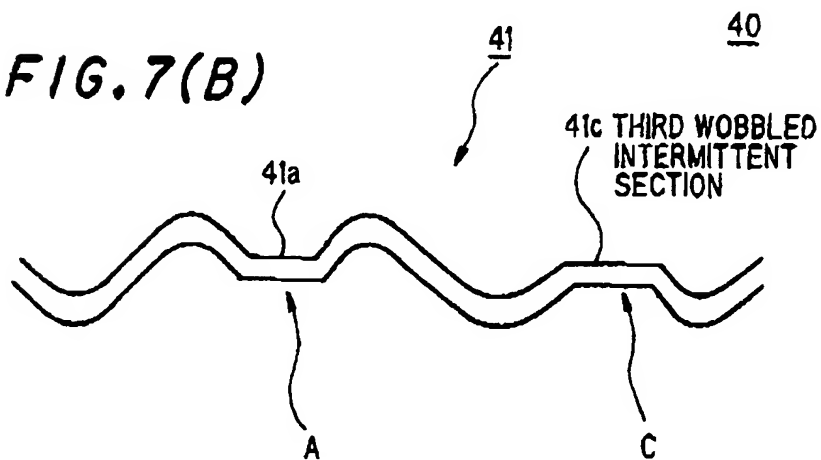
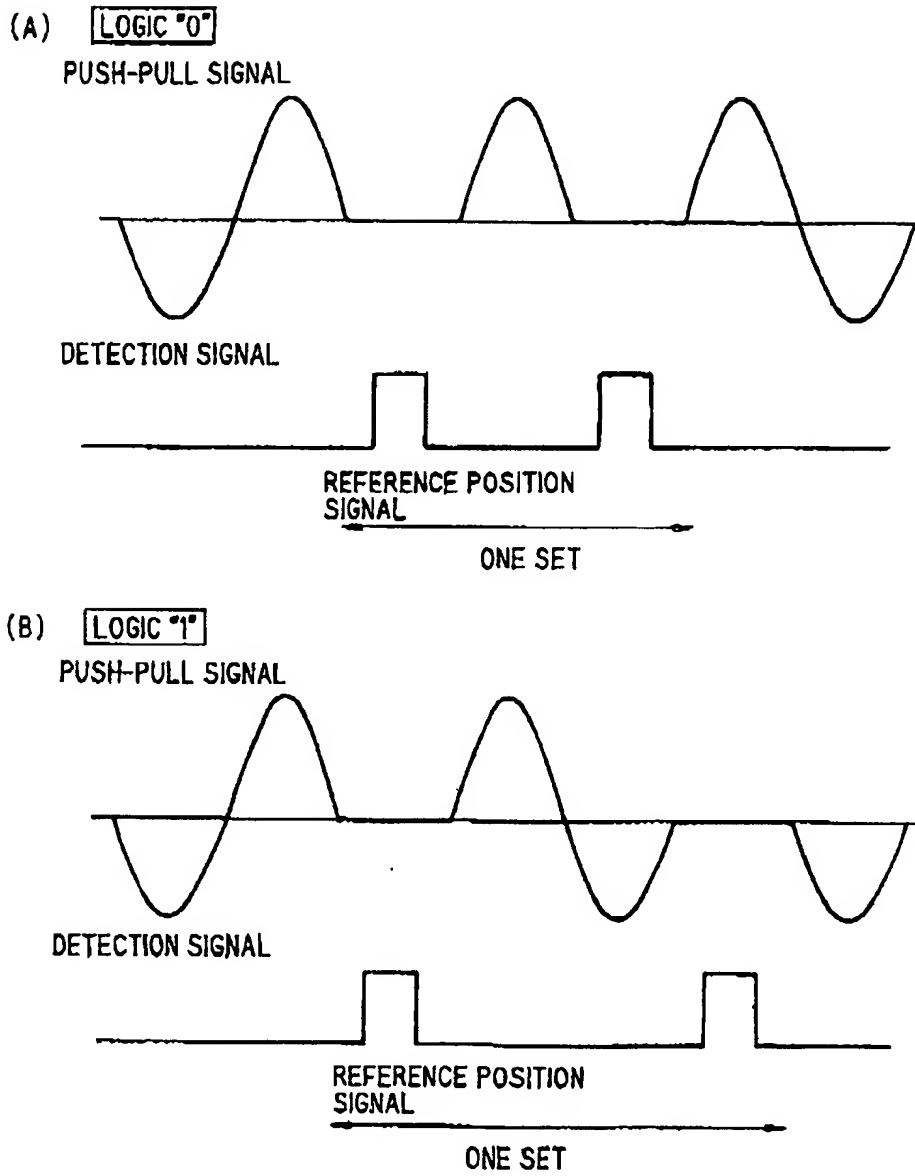


FIG. 8



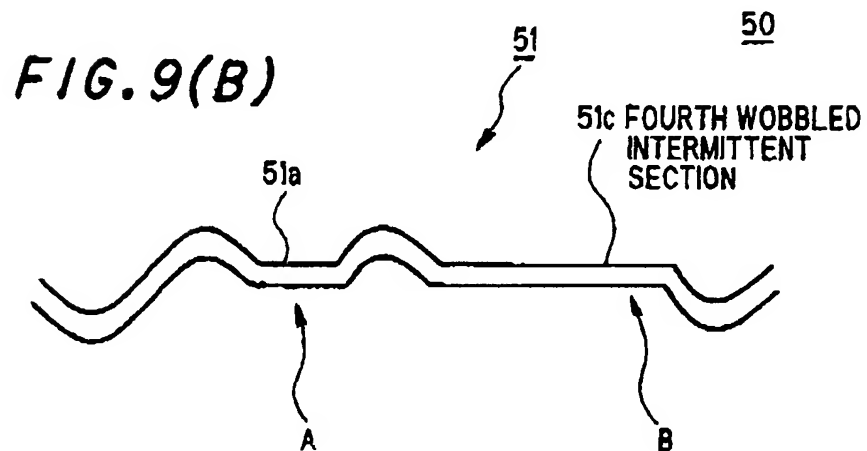
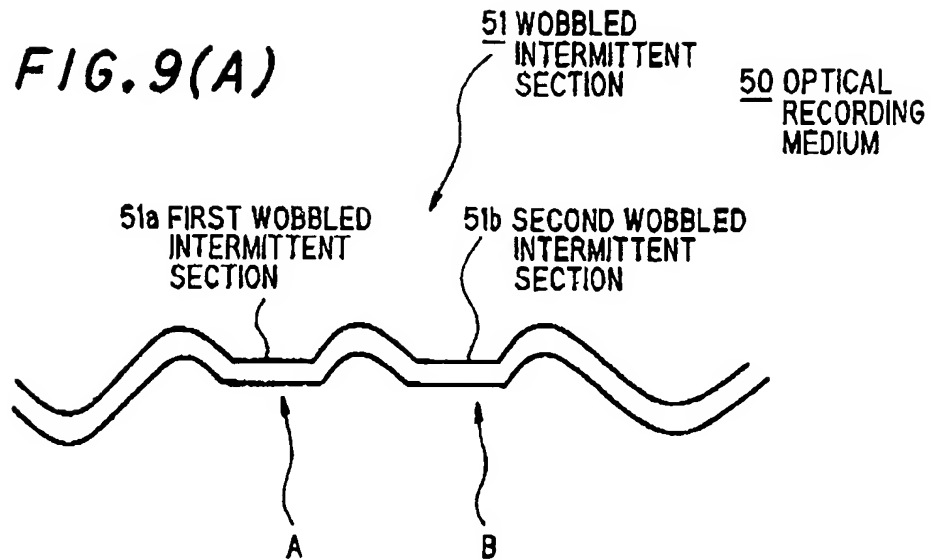
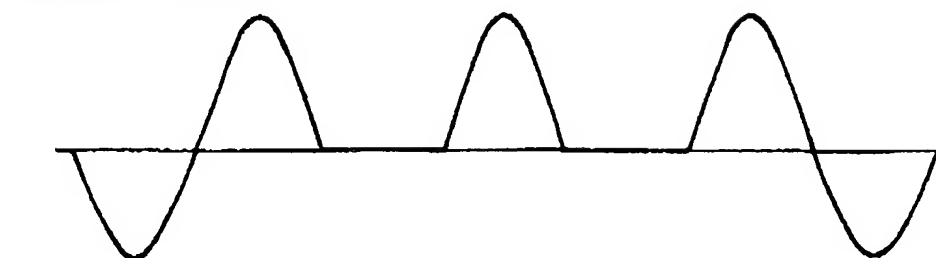


FIG. 10

(A) **LOGIC "0"**

PUSH-PULL SIGNAL



DETECTION SIGNAL



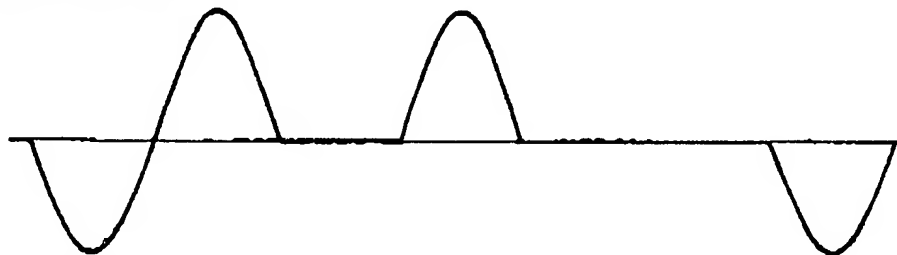
REFERENCE POSITION
SIGNAL



ONE SET

(B) **LOGIC "1"**

PUSH-PULL SIGNAL



DETECTION SIGNAL



REFERENCE POSITION
SIGNAL



ONE SET